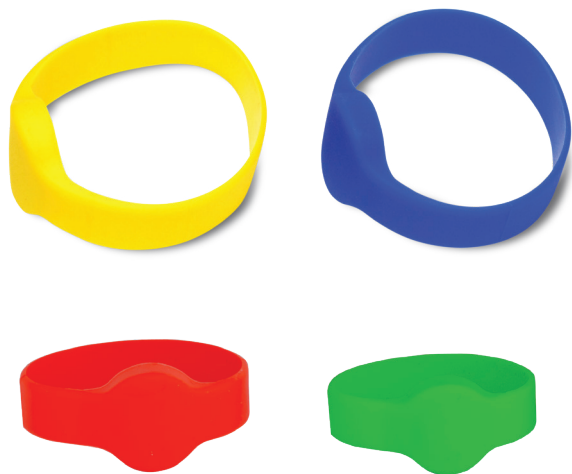




IDT-4001MF

Bratara cu cip de proximitate Mifare S50 (13.56Mhz)

Imagine



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IDT-4001MF
Bratara de proximitate cu cip Mifare S50
(13.56Mhz) rezistenta la apa
FISA TEHNICA

Descriere

IDT-4001MF este o bratara de proximitate cu cip Mifare S50 (13.56Mhz) rezistenta la apa, ce poate fi folosita in diverse aplicatii de control acces, sisteme publice de taxare, sisteme de fidelizare etc. Suprafata neteda acoperita de folie protectoare, precum si designul subtire de doar 0.76mm permit personalizarea acestora.

Caracteristici

- Cip Mifare S50 (13.56Mhz) 1K
- Material: silicon
- Rezistenta la apa
- Reinscriptibil
- Culori disponibile: albastru, verde, rosu, galben

Specificatii

- Functie anti-coliziune
- Distanta de operare: 100mm (depinde de amplasarea antenei)
- Protectie la descarcari electrostatice (1000V)
- Format Wiegand 34
- Securitate sporita
- Timp de procesare al tranzactiei de ticketing mai mic de 100ms
- Rata de transfer: 106 kbit/s
- Memorie EEPROM organizata in 16 sectoare si 4 blocuri (un bloc - 16 biti)
- Perioada de retentie a datelor: 10 ani
- Cicluri de scriere: 100.000
- Temperatura de operare: -40 ~ +65 °C
- Dimensiuni: 45/60(Φ) x 25(l) x 8(A) mm
- Masa bruta: 0.01 kg

Simbol	Parametri	Conditii	Min	Tip	Max	Unitate
C _i	Capacitanta		14.4	16.1	17.4	pF
f _i	Frecventa		-	13.56	-	MHz
Caracteristici EEPROM						
t _{ret}	retentia datelor	amb = 22 °C	10	-	-	an
N _{endu(W)}	scrierea datelor	amb = 22 °C	100000	200000	-	ciclu

T_{amb} = 22 °C, f_i = 13.56 MHz, 2 V RMS.

Timpul de transfer al datelor

	T _{ACK min}	T _{ACK max}	T _{NAK min}	T _{NAK max}	T _{TimeOut}
Transfer	71 μs	T _{TimeOut}	71 μs	T _{TimeOut}	10 ms



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Timpul de scriere a datelor

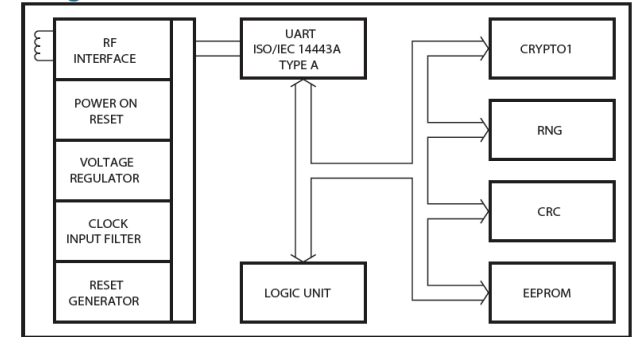
	T _{ACK min}	T _{ACK max}	T _{NAK min}	T _{NAK max}	T _{TimeOut}
Write part 1	71 μs	T _{TimeOut}	71 μs	T _{TimeOut}	5 ms
Write part 2	71 μs	T _{TimeOut}	71 μs	T _{TimeOut}	10 ms

	T _{ACK min}	T _{ACK max}	T _{NAK min}	T _{NAK max}	T _{TimeOut}
Increment, Decrement, and Restore part 1	71 μs	T _{TimeOut}	71 μs	T _{TimeOut}	5 ms
Increment, Decrement, and Restore part 2	71 μs	T _{TimeOut}	71 μs	T _{TimeOut}	5 ms

Organizarea memoriei

Sector	Block	Byte Number within a Block																Description
		0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
15	3	Key A			Access Bits			Key B						Sector Trailer 15				
	2	Data																
	1	Data																
	0	Data																
14	3	Key A			Access Bits			Key B						Sector Trailer 14				
	2	Data																
	1	Data																
	0	Data																
:	:																	
:	:																	
:	:																	
1	3	Key A			Access Bits			Key B						Sector Trailer 1				
	2	Data																
	1	Data																
	0	Data																
0	3	Key A			Access Bits			Key B						Sector Trailer 0				
	2	Data																
	1	Data																
	0	Manufacturer Data																
		Manufacturer Block																

Diagrama blocurilor



Observatii



EEE FAC OBIECTUL UNEI
 COLECTARI SEPARATE

